



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

AF/2825

Application of: Zhaohui Shen et al.

Serial No.: 09/684,868

Title: DIAGNOSTIC ARCHITECTURE USING FPGA CORE IN
SYSTEM ON A CHIP DESIGN

Filed: October 6, 2000

Attorney Docket No.: 00-255 / 1496.00039

Examiner: Dinh, P.

Art Unit: 2825

In Response To: Office Action mailed November 4, 2003

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TECHNOLOGY CENTER 2800

I hereby certify that this letter, the response or amendment attached hereto are being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on February 4, 2004.

By: 
Jan M. Dunbar

AMENDMENT AFTER FINAL

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Sir:

In response to the ^{Final} Office Action mailed November 4, 2003

please amend the above-identified application as follows:

DO NOT
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P